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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	Unknown /
Filing Date	October 06, 2003
First Named Inventor	Schuyler E. Shimanek
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1271 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 6,388,939 B1	05-14-02	Manapat et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Foreign Patent Document Country Code [3] -Number [4] -Kind Code [5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T [6]

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[1] Applicant's unique citation designation number (optional). [2] See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known	
Application / Conf. N.	Unknown /
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Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1271 US

OTHER -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		BRIAN DIPERT, "Special-purpose SRAMs smooth the ride," June 24, 1999, pages 93-104, available from www.ednmag.com/InfoAccess.	
		"Week 03: Memory and Caches," EE453 ADVANCED COMPUTER ARCHITECTURE, September 20, 2001, pages 1-15, available from internet at www.ee.cooper.edu/~loh/ee453/notes/week03.pdf .	
		V. DEGALAHAL, "Analyzing Soft Errors in Leakage Optimized SRAM Design," Proceedings of 16th International Conference on VLSI Design, 2003, pages 1-11, available from Microsystems Design Laboratory, Pennsylvania State University, E-mail: {degalaha,vijay,mji}@cse.psu.edu.	

Examiner Signature		Date Considered	

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